



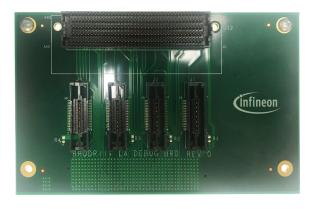
Product brief

Logic analyzer mezzanine card for RadHard QDR®-II+ SRAM debug

CYDK-QDRII-LA

Mezzanine card for connecting a logic analyzer to debug the RadHard QDR®-II+ SRAM interface to the CYDK-QDRII-NODT

This LA card has been developed to work with the Infineon CYDK-QDRII-NODT Mezzanine card to provide a path to debug the interface between the FPGA and RadHard QDR®-II+ SRAM. The board is developed and tested to work with FPGA carrier cards through the FMC-HPC connector.



Key features

- > 250 MHz frequency
- > 72/144-Mbit density
- > x18 or x36 data bus width
- > Brings all SRAM interface signals to
- > Provides standard 40 pin Mictor connectors for LA single ended probes

Product overview

Advantages

- > Supports all Infineon RadHard 72 & 144-Mbit QDR®-II+ SRAMs
- Ability to observe all QDR®-II+ SRAM signals at the same time on the logic analyzer

For further details

To learn more about RH QDR®-II+ and all Infineon RH products, visit www.infineon.com/hirelmemory To purchase RH QDR®-II+ parts, visit www.cypress.com/buyonline

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