



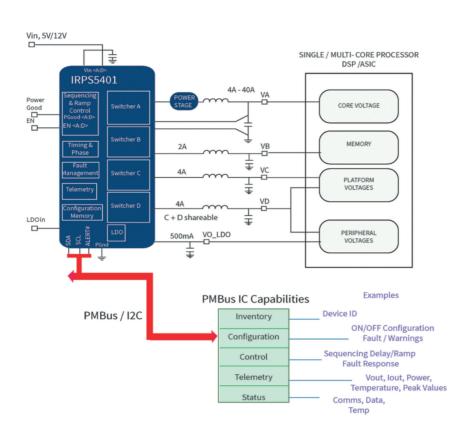
## **Product Brief**

# Multi-Output PMIC with Integrated Sequencer

IRPS5401

The IRPS5401 is an integrated power management IC ideal for tight board space requirements; for example, FPGAs, System On Chip ASICs and multi-core processors containing several voltages that require precision accuracy and voltage sequencing.

# Typical PMIC Configuration



# Key features

- Multi-output DC/DC with integrated FETs and Sequencer
- > Replace many regulators with one PMIC
  - 4 switchers and 1 LDO in one package
  - Output A: 2 A (without), 50 A (with power stage)
  - Output B: 2 A
  - Outputs C,D: 4 A
  - Linear regulator: +/-0.5 A
- > Full PMBus: Margining, Fault Management, Telemetry









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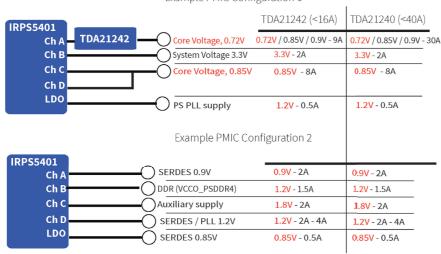


# Multi-Output PMIC with Integrated Sequencer

IRPS5401

# Scalable Power Options for Power Design Flexibility

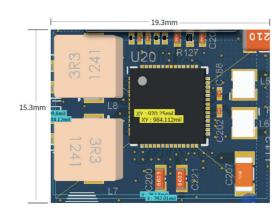
Example PMIC Configuration 1



### Compact Power Solutions Includes Sequencer

# Infineon Compact Power Design for FPGA/SoCs, IRPSS401

### Layout savings



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