

## Mapping of OPTIREG<sup>™</sup> product portfolio with various microcontrollers

ATV SP APS H2 2024

#### **Important Notice**



#### Ownership concerns and suggestions

- The ownership of this document lies with ATV SP APS PMG team.
- Please refrain from creating versions of this document, modifying its content or indulging into any kind of editing, instead contact us!
- We are constantly improving this document, therefore if you have any suggestions, feedback, complaints, questions, ideas, etc. please write us
  an e-mail with subject title "mapping table" to the address: R-IFX-APSProductMar@infineon.com

#### How to use this document?

- The aim of this document is to give an overview of the fit between OPTIREG<sup>™</sup> products and various microcontrollers. For detailed information please refer to datasheets.
- On the upcoming slide you will find a navigation table, to jump to the slide with information of your interest. Once you are on the slide you can click on the ,Home' icon to return to the navigation table.
- We have classified the quality of fit into various categories. On the slide ,Legend for Fit Description' you will find more information about the fit types and respective description. On individual slides we have also mentioned what type of fit exists for a particular power supply product and microcontroller.

### **Navigation Table**





CLICK!	Infineon A	URIX™	Infineon <sup>-</sup>	Traveo™	Infineon	Texas Instruments	NXP	Renesas	ST Micro
OPTIREG™	TC2x	TC3x	- 1	II	PSoC®	Piccolo™/ Delfino™	S32K	RH850	SPC5x
OPTIREGTH PMIC	<b>6</b>	<b>©</b>	<b>©</b>	<b>©</b>	N/A	<b></b>	<b>©</b>	6	<b>6</b>
OPTIREG™ Linear	<b>©</b>	<b>©</b>	Ø	Ø	<b>©</b>	<b>6</b>	<b>©</b>	N/A	N/A
OPTIREG™ Switcher	<b>©</b>	<b>©</b>	Ø	<b>©</b>	N/A	<b></b>	Ø	N/A	N/A
OPTIREG™  SBC  WINTER  SOCIOR  SOCIOR	<b>©</b>	<b>©</b>	Ø	<b>©</b>	<b>©</b>	<b>©</b>	<b>©</b>	<b>©</b>	<b>6</b>



### **Legend for Fit Description**

Symbol	Fit type	Description
Ø	Best Fit	Everything fits <b>perfectly</b> between the power supply product and respective microcontroller.
	Fit	The power supply fits well but may be more suitable for a microcontroller with <b>higher</b> power requirements; additional small componets (capacitor, BJT,) might be needed.
	Fit with additional components	Based on voltage requirement of the microcontroller an additional post LDO or a DC-DC may be required (µC without internal VREG).
	(blank)	No fit



## Mapping OPTIREG™ product portfolio with Infineon AURIX™ Microcontrollers

## Mapping OPTIREG™ linear with AURIX™ TC2x Microcontroller







		Maximum Power Requirements (real power pattern)	OPTIREG™ linear								
	Infineon AURIX <sup>TM</sup>		Ultra	a Low Quiescent Curre	nt		Feature Set I Watchdog	Post LDO / Core Voltage			
	Family		TLS810xxx	TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx	TLS20x Family			
	·		100mA (5V/3.3V)	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)	Use in combination with pre dc-dc			
	TC21 series	88mA @ 3.3V	Ø								
	TC22 series	88mA @ 3.3V	Ø			<b>⊘</b>					
	TC23 series	109mA @ 3.3V		<b>©</b>	<b>©</b>	<b>©</b>					
T00	TC26 series	186mA @ 3.3V 123mA @ 5V		<b>⊘</b>			<b>⊘</b>	TLS203x (300mA)			
TC2x	TC26 series (ADAS variant)	203mA @ 3.3V 134mA @ 5V						TLS203x (300mA)			
	TC27 series	307mA @ 3.3V 203mA @ 5V						TLS203x (300mA)			
	TC29 series	485mA @ 3.3V 320mA @ 5V						TLS205x (500mA)			
	TC29 series (ADAS variant)	515mA @ 3.3V 340mA @ 5V									







### Mapping OPTIREG™ switcher with AURIX™ TC2x Microcontroller







				OPTIREG™ switcher for Pre-Regulation and Core Voltages							
	Infineon	Mariana Barra Biarinatian		12V Pre-Regulator Low Power DC-DC 500mA			12V Pre-Regulator Medium Power Up-to 2.5A				
	AURIX™ Family	Maximum Power Dissipation (real power pattern)	TLF50241	TLF50251	TLF50281	TLS4120D0EP V33	TLS4125D0EP V50	TLS20x Family			
			500mA (5V)	500mA (5V)	500mA (5V)	2000mA (3.3V)	2500mA (5V)	Use in combination			
			Reset	Enable & Reset	Enable, Reset & Watchdog	Enable + Reset	Enable + Reset	with pre dc-dc			
	TC21 series	88mA @ 3.3V									
	TC22 series	88mA @ 3.3V									
	TC23 series	109mA @ 3.3V									
TC2x	TC26 series	186mA @ 3.3V 123mA @ 5V									
TGZX	TC26 series (ADAS variant)	203mA @ 3.3V 134mA @ 5V						TLS203x (300mA)			
	TC27 series	307mA @ 3.3V 203mA @ 5V	<b>Ø</b>	<b>Ø</b>				TLS203x (300mA)			
	TC29 series	485mA @ 3.3V 320mA @ 5V						TLS203x (300mA)			
	TC29 series (ADAS variant)	515mA @ 3.3V 340mA @ 5V			<b>⊘</b>	<b>Ø</b>		TLS203x (300mA)			





Fit with additional components

Note:Based on **Maximum Power Dissipation** (at real power pattern) see datasheet parameter PD SR ( $T_J = 150$ °C); Current Value = Power Dissipation / Voltage Level;

Further support and calculation tools under www.infineon.com/OPTIREG™ and www.infineon.com/AURIX™

## Mapping OPTIREG™ SBC with AURIX™ TC2x Microcontroller







					OPTIREG™ SBC		
	nfineon AURIX™	Maximum Power Dissipation	Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC
	Family	(real power pattern)	TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX
			150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)
	TC21 series	88mA @ 3.3V	Ø		<b>©</b>		
	TC22 series	88mA @ 3.3V	<b>Ø</b>		Ø		
	TC23 series	109mA @ 3.3V	<b>©</b>		Ø		
TC2x	TC26 series	186mA @ 3.3V 123mA @ 5V		<b>Ø</b>	Ø		
TC2X	TC26 series (ADAS variant)	203mA @ 3.3V 134mA @ 5V			Ø		
	TC27 series	307mA @ 3.3V 203mA @ 5V		$\bigcirc$			
	TC29 series	485mA @ 3.3V 320mA @ 5V					
	TC29 series (ADAS variant)	515mA @ 3.3V 340mA @ 5V					







### Mapping OPTIREG™ PMIC with AURIX™ TC2x Microcontroller







					OPTIREG™ PMIC	
	Infineon AURIX™	Maximum Current Consumption	ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant
	Family	(real power pattern)	TLF35584/5Q*	TLF35584/5Q* w/ TLF11251	TLF30682QV	TLE9243QK
	TC21 series	88mA @ 3.3V				
	TC22 series	88mA @ 3.3V				
	TC23 series	109mA @ 3.3V				
TC2x	TC26 series	186mA @ 3.3V 123mA @ 5V				
1028	TC26 series (ADAS variant)	203mA @ 3.3V 134mA @ 5V				
	TC27 series	307mA @ 3.3V 203mA @ 5V	<b>Ø</b>			$\bigcirc$
	TC29 series	485mA @ 3.3V 320mA @ 5V	Ø			
	TC29 series (ADAS variant)	515mA @ 3.3V 340mA @ 5V	Ø		<b>⊘</b>	$\bigcirc$







## Mapping OPTIREG™ linear with AURIX™ TC3x Microcontroller







		Maximum Power			OPTIREG <sup>TI</sup>	<sup>™</sup> linear		
	Infineon AURIX™		Ultra	Low Quiescent Curre	ent		Feature Set I Watchdog	Post LDO / Core Voltage
•	Family	Requirements (real power pattern)	TLS810xxx	TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx	TLS20x Family
			100mA (5V/3.3V)	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)	Use in combination with pre dc-dc
	TC33 series	200mA @ 3.3V 132mA @ 5V						TLS202x (150mA)
	TC33 series (ADAS variant)	381mA @ 3.3V 252mA @ 5V						TLS203x (300mA)
	TC35 series	576mA @ 3.3V 380mA @ 5V						
Ond	TC36 series	333mA @ 3.3V 240mA @ 5V						TLS203x (300mA)
2 <sup>nd</sup> Gen	TC37 series	370mA @ 3.3V 244mA @ 5V						TLS205x (500mA)
	TC38 series	515mA @ 3.3V 340mA @ 5V						
	TC39 series	758mA @ 3.3V 500mA @ 5V						
	TC39 series (ADAS variant)	679 mA @ 3.3V (T <sub>J</sub> = 125°C) 448 mA @ 5V (T <sub>J</sub> = 125°C)						







### Mapping OPTIREG<sup>™</sup> switcher with AURIX<sup>™</sup> TC3x Microcontroller







				OF	PTIREG™ switcher for Pre-	Regulation and Core Vol	tages	
	Infineon	Maximum Davier Discinction		12V Pre-Regulator Low Power DC-DC 500mA			lator o-to 2.5A	Post LDO / Core Voltage
	AURIX™ Family	Maximum Power Dissipation (real power pattern)	TLF50241	TLF50251	TLF50281	TLS4120D0EP V33	TLS4125D0EP V50	TLS20x Family
			500mA (5V)	500mA (5V)	500mA (5V)	2000mA (3.3V)	2500mA (5V)	Use in combination
			Reset	Enable & Reset	Enable, Reset & Watchdog	Enable + Reset	Enable + Reset	with pre dc-dc
	TC33 series	200mA @ 3.3V 132mA @ 5V						TLS203x (300mA)
	TC33 series (ADAS variant)	381mA @ 3.3V 252mA @ 5V			<b>⊘</b>			TLS203x (300mA)
	TC35 series	576mA @ 3.3V 380mA @ 5V						TLS205x (500mA)
TC3x	TC36 series	333mA @ 3.3V 240mA @ 5V			<b>⊘</b>			TLS205x (500mA)
103x	TC37 series	370mA @ 3.3V 244mA @ 5V						TLS203x (300mA)
	TC38 series	515mA @ 3.3V 340mA @ 5V			<b>⊘</b>	$\bigcirc$		TLS203x (300mA)
	TC39 series	758mA @ 3.3V 500mA @ 5V						TLS205x (500mA)
	TC39 series (ADAS variant)	679 mA @ 3.3V (T <sub>J</sub> = 125°C) 448 mA @ 5V (T <sub>J</sub> = 125°C)				<b>⊘</b>		TLS205x (500mA)





Fit with additional components

Note:Based on **Maximum Power Dissipation** (at real power pattern) see datasheet parameter PD SR ( $T_J = 150$ °C); Current Value = Power Dissipation / Voltage Level;

Further support and calculation tools under www.infineon.com/OPTIREG™ and www.infineon.com/AURIX™

## Mapping OPTIREG™ SBC with AURIX™ TC3x Microcontroller







					OPTIREG™ SBC		
	nfineon AURIX™	Maximum Power Dissipation	Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC
Family		(real power pattern)	TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX
			150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)
	TC33 series	200mA @ 3.3V 132mA @ 5V			<b>Ø</b>		
	TC33 series (ADAS variant)	381mA @ 3.3V 252mA @ 5V			<b>Ø</b>	<b>⊘</b>	<b>Ø</b>
	TC35 series	576mA @ 3.3V 380mA @ 5V					
TC2v	TC36 series	333mA @ 3.3V 240mA @ 5V				<b>Ø</b>	
TC3x	TC37 series	370mA @ 3.3V 244mA @ 5V					
	TC38 series	515mA @ 3.3V 340mA @ 5V					
	TC39 series	758mA @ 3.3V 500mA @ 5V					
	TC39 series (ADAS variant)	679 mA @ 3.3V (T <sub>J</sub> = 125°C) 448 mA @ 5V (T <sub>J</sub> = 125°C)					







### Mapping OPTIREG™ PMIC with AURIX™ TC3x Microcontroller







			OPTIREG™ PMIC						
Infineon AURIX™		Maximum Current Consumption	ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant			
	Family	(real power pattern)	TLF35584/5Q*	TLF35584/5Q* w/ TLF11251	TLF30682QV	TLE9243QK			
	TC33 series	200mA @ 3.3V 132mA @ 5V							
	TC33 series (ADAS variant)	381mA @ 3.3V 252mA @ 5V	<b>©</b>		Ø				
	TC35 series	576mA @ 3.3V 380mA @ 5V	<b>Ø</b>						
TC3x	TC36 series	333mA @ 3.3V 240mA @ 5V	<b>©</b>		<b>©</b>				
ICSX	TC37 series	370mA @ 3.3V 244mA @ 5V	<b>©</b>		<b>Ø</b>				
	TC38 series	515mA @ 3.3V 340mA @ 5V	<b>Ø</b>	Ø					
	TC39 series	758mA @ 3.3V 500mA @ 5V		<b>Ø</b>					
	TC39 series (ADAS variant)	679 mA @ 3.3V (T <sub>J</sub> = 125°C) 448 mA @ 5V (T <sub>J</sub> = 125°C)	<b>Ø</b>	Ø		<b>⊘</b>			









# Mapping OPTIREG™ product portfolio with Infineon Traveo™ Microcontrollers

## Mapping OPTIREG<sup>™</sup> linear with Traveo<sup>™</sup> Microcontroller







					OPTIREG™	linear		
1	Fraveo™	Maximum Power Supply Current	Ultra	a Low Quiescent Currer	nt		Feature Set d Watchdog	Post LDO / Core Voltage
	Family		TLS810xxx	TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx	TLS20x Family
			100mA (5V/3.3V)	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)	Use in combination with pre dc-dc
	S6J311A/9 series	5V / 200mA (T <sub>A</sub> = -40°C to +125°C)		<b>Ø</b>	<b>Ø</b>	<b>Ø</b>	Ø	
	S6J311E/D series	5V / 343mA (T <sub>A</sub> = -40°C to +105°C)			<b>©</b>		<b>©</b>	
	S6J3120 series	5V / 255mA (T <sub>A</sub> = -40°C to +105°C)			<b>Ø</b>		<b>©</b>	
1 <sup>st</sup> Gen	S6J3360 series	5V / 174mA (T <sub>A</sub> = -40°C to +105°C)		<b>Ø</b>	Ø	<b>Ø</b>		
	S6J3370 series	5V / 158mA (T <sub>A</sub> = -40°C to +105°C)		<b>Ø</b>		Ø		
	S6J3400 series	5V / 150mA (T <sub>A</sub> = -40°C to +125°C)		Ø		<b>Ø</b>	<b>Ø</b>	
	S6J3510 series	<b>5V / 158mA</b> (T <sub>A</sub> = -40°C to +125°C)		<b>Ø</b>		<b>Ø</b>		







## Mapping OPTIREG<sup>™</sup> switcher with Traveo<sup>™</sup> Microcontroller







				OPTIREG™ switcher for Pre-Regulation and Core Voltages							
	Traveo™	Maximum Power		12V Pre-Reg Low Power DC-I		12V Pre-Regulator Medium Power Up-to 2.5A					
	Family	Supply Current	TLF50241	TLF50251	TLF50281	TLS4120D0EP V33	TLS4125D0EP V50				
			500mA (5V)	500mA (5V)	500mA (5V)	2000mA (3.3V)	2500mA (5V)				
			Reset	Enable & Reset	Enable, Reset & Watchdog	Enable + Reset	Enable + Reset				
	S6J311A/9 series	<b>5V / 200mA</b> (T <sub>A</sub> = -40°C to +125°C)	<b>Ø</b>	<b>Ø</b>	<b>Ø</b>						
	S6J311E/D series	5V / 343mA (T <sub>A</sub> = -40°C to +105°C)	<b>Ø</b>	Ø	Ø	<b>Ø</b>					
	S6J3120 series	<b>5V / 255mA</b> (T <sub>A</sub> = -40°C to +105°C)	<b>©</b>	<b>Ø</b>	<b>©</b>						
1 <sup>st</sup> Gen	S6J3360 series	5V / 174mA (T <sub>A</sub> = -40°C to +105°C)									
	S6J3370 series	5V / 158mA (T <sub>A</sub> = -40°C to +105°C)									
	S6J3400 series	5V / 150mA (T <sub>A</sub> = -40°C to +125°C)									
	S6J3510 series	<b>5V / 158mA</b> (T <sub>A</sub> = -40°C to +125°C)									







Note: Based on **Power Supply Current Flash max.**, see datasheet parameter I<sub>CC5</sub>

Further support and calculation tools under <u>www.infineon.com/OPTIREG™</u>

## Mapping OPTIREG™ SBC with Traveo™ Microcontroller







					OPTIREG™ SBC		
٦	Traveo™	Maximum Power Supply Current	Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC
	Family		TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX
			150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)
	S6J311A/9 series	5V / 200mA (T <sub>A</sub> = -40°C to +125°C)		Ø	<b>©</b>		
	S6J311E/D series	5V / 343mA (T <sub>A</sub> = -40°C to +105°C)		Ø			
	S6J3120 series	5V / 255mA (T <sub>A</sub> = -40°C to +105°C)		<b>©</b>	<b>Ø</b>		
1 <sup>st</sup> Gen	S6J3360 series	5V / 174mA (T <sub>A</sub> = -40°C to +105°C)			<b>Ø</b>		
	S6J3370 series	5V / 158mA (T <sub>A</sub> = -40°C to +105°C)	<b>Ø</b>		<b>Ø</b>		
	S6J3400 series	5V / 150mA (T <sub>A</sub> = -40°C to +125°C)	<b>©</b>		<b>Ø</b>		
	S6J3510 series	5V / 158mA (T <sub>A</sub> = -40°C to +125°C)	Ø		<b>©</b>		







### Mapping OPTIREG™ PMIC with Traveo™ Microcontroller







				OP.	TIREG™ PMIC	
	Traveo™ Family	Maximum Current Consumption	ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant
		(real power pattern)	TLF35584QV/QK/ QVH	TLF35585QV/QU	TLF30682QV	TLE9243QK
	S6J311A/9 series	<b>5V / 200mA</b> (T <sub>A</sub> = -40°C to +125°C)				
	S6J311E/D series	5V / 343mA (T <sub>A</sub> = -40°C to +105°C)	Ø	<b>Ø</b>		<b>©</b>
	S6J3120 series	5V / 255mA (T <sub>A</sub> = -40°C to +105°C)				
1 <sup>st</sup> Gen	S6J3360 series	5V / 174mA (T <sub>A</sub> = -40°C to +105°C)	<b>⊘</b>	<b>⊘</b>	<b>⊘</b>	
	S6J3370 series	5V / 158mA (T <sub>A</sub> = -40°C to +105°C)				
	S6J3400 series	<b>5V / 150mA</b> (T <sub>A</sub> = -40°C to +125°C)	<b>⊘</b>	$\bigcirc$		
	S6J3510 series	5V / 158mA (T <sub>A</sub> = -40°C to +125°C)				







### Mapping OPTIREG<sup>™</sup> Linear/Switcher with Traveo<sup>™</sup> II Microcontroller









				Current consumption				OPTIREG™	Linear		OPTIREG™ Switcher		OPTIREG™
	TRAVEOUR TOO		Current consumption  Max. as per datasheet	Scenario with lower	I <sub>rush</sub>	supply .	Ultra Low Quiescent Current		Advanced Feature Set Reset and Watchdog		12V Pre-Regulator Medium Power with enable + reset		Core Voltage
	TRAVEO™ T2G Family	Core #	Max. clock speed All peripherals enabled	performance at  T <sub>A</sub> = 85°C	C <sub>S1</sub>		TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx	TLS4120D0x	TLS4125D0x	
ı	· uniny	၁	No I/O toggling  T <sub>A</sub> = 105°C	Max. as per estimation sheet Note: Flash writing not considered	*	and supply concept	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)	2000mA (3.3V)	2500mA (5V)	1.15V
t	<b>T2G-BE-512K</b> CYT2B6		I <sub>DDD</sub> = 85mA @3.3V / 5V	I <sub>DDD</sub> = 45mA				_	_		_	_	
/ entry	T2G-BE-1M CYT2B7		$I_{DDD} = 102 \text{mA} @ 3.3 \text{V} / 5 \text{V}$	$I_{10} = 4mA$				<b>Ø</b>		<b>(6)</b>			
Bodv	T2G-BE-2M CYT2B9 T2G-BE-4M CYT2BL	<u>ө</u>	$I_{DDD} = 110 \text{mA} @ 3.3 \text{V} / 5 \text{V}$ $I_{DDD} = 127 \text{mA} @ 3.3 \text{V} / 5 \text{V}$	Door ECU	375mA 9.4µF								
Cluster	T2G-CE-4M CYT2CL	Single	I <sub>DDD</sub> = 140mA @ 3.3V / 5V	$I_{DDD} = 52mA$ $I_{IO} = 4mA$ Cluster entry	·	Ü		<b>©</b>		<b>Ø</b>			
	T2G-BH-4M CYT3BB		I <sub>CCD</sub> = 240mA @ 1.15V	$I_{CCD} = 110 \text{mA}$ $I_{DDD} = 12 \text{mA}$ $I_{IO} = 2 \text{mA}$		Yes							Post LDO TLS208
high	T2G-BH-4M CYT4BB	al	I <sub>CCD</sub> = 287mA @ 1.15V	Cluster ECU	850mA	① ②③							800mA
Body high	T2G-BH-8M CYT4BF	Dual	I <sub>CCD</sub> = 543mA @ 1.15V	I <sub>CCD</sub> = 320mA	20μF Yes	Yes ②③							Post LDO TLS208
	<b>T2G-BH-16M</b> CYT6BJ	Quad	I <sub>CCD</sub> = 800mA @ 1.15V			Yes ③							800mA







(1)(2)(3) Supply concept like defined in further slides

Note: \* In-rush current through internal regulator to charge capacitor C<sub>S1</sub> on core supply rail initially. Value for C<sub>S1</sub> is a typical value. May differ due to application needs

Further support and calculation tools under <u>www.infineon.com/OPTIREG™</u>

### Mapping OPTIREG™ SBC with Traveo™ II Microcontroller





				Current				System	Basis Chip (S	SBC)		OPTIREG™	
				Current	consumption Scenario with		Extern al core		Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC	Core supply
	T	RAVEO™ T2G	Core #	consumption	lower performance at	I <sub>rush</sub> C <sub>S1</sub>	supply	TLE9461(-3)ES	TLE9471(-3)ES	TLE926x(-3) BQX	TLE927xQX	TLE9278(-3) BQX	
	Family		ပိ	Max. as per datasheet Max. clock speed All peripherals enabled No I/O toggling T <sub>A</sub> = 105°C	Max. as per estimation sheet Note: Flash writing not considered		and supply concep t		500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)	1.15V
	Ę	<b>T2G-BE-512K</b> CYT2B6		$I_{DDD} = 85 \text{mA} @ 3.3 \text{V} / 5 \text{V}$	$I_{DDD} = 45 \text{mA}$								
	/ en	T2G-BE-1M CYT2B7		$I_{DDD} = 102 \text{mA} @ 3.3 \text{V} / 5 \text{V}$	$I_{IO} = 4mA$			<b>(6)</b>	<b>Ø</b>	<b>(6)</b>			
	Body entry	T2G-BE-2M CYT2B9 T2G-BE-4M CYT2BL	<u>e</u>	$I_{DDD} = 110 \text{mA} @ 3.3 \text{V} / 5 \text{V}$ $I_{DDD} = 127 \text{mA} @ 3.3 \text{V} / 5 \text{V}$	Door ECU	375mA 9.4µF	No 1						
Cluster	entry	T2G-CE-4M CYT2CL	Single	I <sub>DDD</sub> = 140mA @ 3.3V / 5V	$I_{DDD} = 52mA$ $I_{IO} = 4mA$ Cluster entry			<b>©</b>	<b>©</b>	<b>©</b>			
		T2G-BH-4M CYT3BB		I <sub>CCD</sub> = 240mA @ 1.15V	$I_{CCD} = 110 \text{mA}$ $I_{DDD} = 12 \text{mA}$ $I_{IO} = 2 \text{mA}$		Yes						
	Body high	T2G-BH-4M CYT4BB	Dual	I <sub>CCD</sub> = 287mA @ 1.15V	Cluster ECU	850mA	(1) (2)(3)						Post LDO TLS208
(	Body	T2G-BH-8M CYT4BF	۵	I <sub>CCD</sub> = 543mA @ 1.15V	I <sub>CCD</sub> = 320mA	20μF	Yes ②③						800mA
		<b>T2G-BH-16M</b> CYT6BJ	Quad	I <sub>CCD</sub> = 800mA @ 1.15V	Tbd		Yes 3						







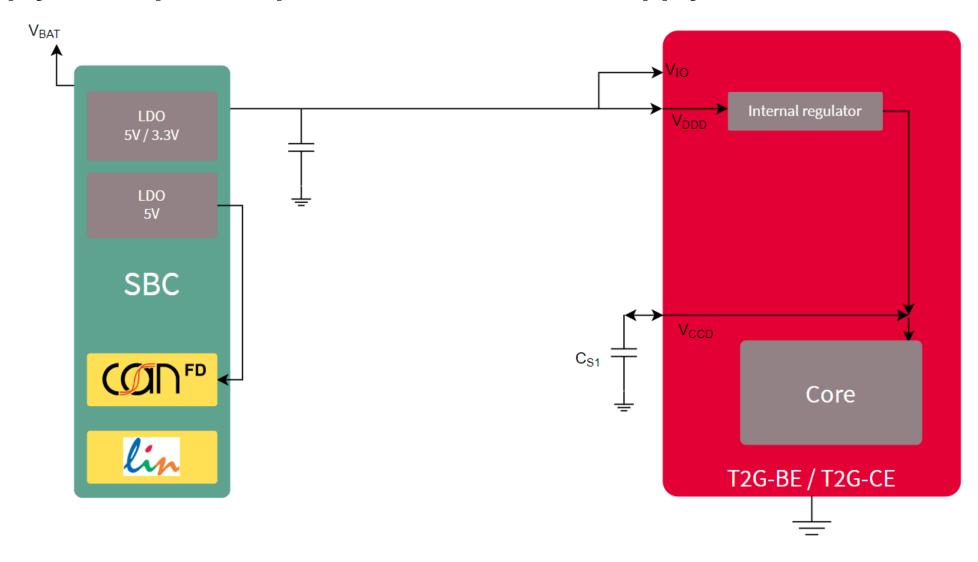
(1)(2)(3) Supply concept like defined in further slides

Note: \* In-rush current through internal regulator to charge capacitor  $C_{S1}$  on core supply rail initially. Value for  $C_{S1}$  is a typical value. May differ due to application needs

Further support and calculation tools under <u>www.infineon.com/OPTIREG™</u>

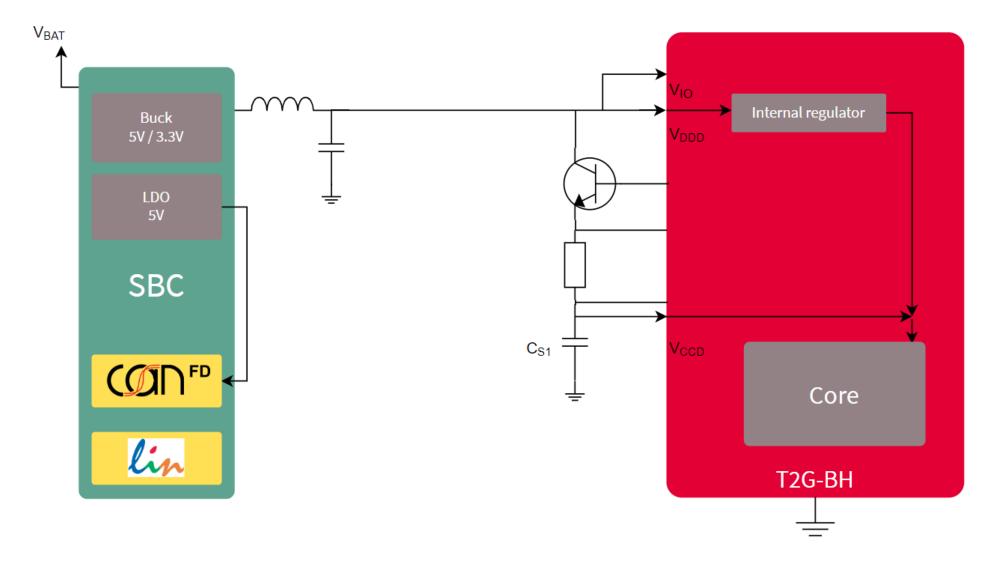


### 1: Supply concept example with internal core supply



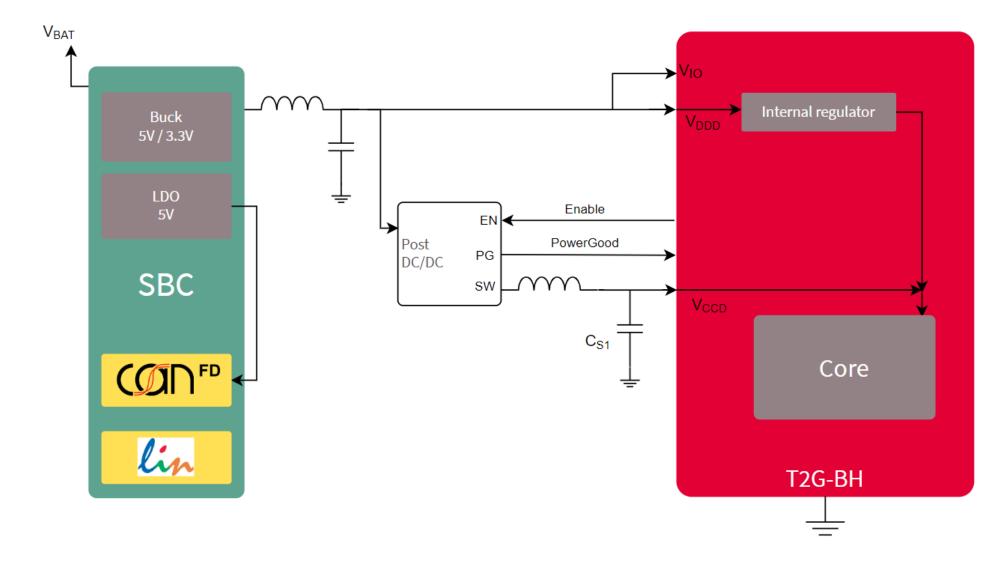


#### 2: Supply concept example with external pass transistor





#### 3: Supply concept example with external core supply



### Mapping OPTIREG™ PMIC with Traveo™ II Microcontroller







		Maximum Current	Need of			OPTIREG™ PMIC		OPTIREG™ linear & switcher
	aveo™ II Family	Consumption (real power	external	ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant	Core Voltage
	, <b>,</b>	pattern)	supply	TLF35584QV/QK/ QVH	TLF35585QV/QU	TLF30682QV	TLE9243QK	1.15V
	TVII-B-E-512K CYT2B6 TVII-B-E-1M CYT2B7	IDDD = 102mA @3.3V / 5V IIO = 2 x 15mA @ 3.3V / 5V	No					
	TVII-B-E-2M CYT2B9 TVII-B-E-4M CYT2BL	IDDD = 110mA @3.3V / 5V IIO = 2 x 15mA @ 3.3V / 5V	No					
2 <sup>nd</sup>	TVII-B-H-4M	IDDD= 249mA @ 3.3V / 5V IIO = 2 x 15mA @ 3.3V / 5V	No	<b>Ø</b>	<b>⊘</b>	<b>⊘</b>	<b>⊘</b>	
Gen.		ICCD = 240mA @ 1.15V IDDD= 9mA @ 3.3V / 5V IIO = 2 x 15mA @ 3.3V / 5V IIOHS = 1 x 20mA @ 3.3V	Yes	<b>Ø</b>	<b>⊘</b>		<b>⊘</b>	Post LDO TLS208 800mA
	TVII-B-H-8M CYT4BF	ICCD = 431mA @ 1.15V IDDD = 9.3mA @ 3.3V / 5V IIO = 2 x 15 mA @ 3.3V / 5V IIOHS = 2 x 20mA @ 3.3V	Yes	<b>Ø</b>	<b>Ø</b>	<b>⊘</b>	<b>⊘</b>	Post LDO TLS208 800mA
	TVII-C-2D-6M CYT4D	ICCD = 1300mA @ 1.15V (with VIDEOSS) IIO = 2 x 15mA @ 3.3V / 5V IIOHS = 2 x 20mA @ 3.3V	Yes			<b>⊘</b>		DCDC TLE8366EV 1A





Fit with additional components

Note: \* In-rush current through internal regulator to charge capacitor  $C_{S1}$  on core supply rail initially. Value for  $C_{S1}$  is a typical value. May differ due to application needs

Further support and calculation tools under <a href="www.infineon.com/OPTIREG™">www.infineon.com/OPTIREG™</a>



# Mapping OPTIREG™ linear & SBC with Infineon PSoC® 4 Microcontrollers

### Mapping OPTIREG™ linear & SBC with PSoC® 4 Microcontrollers









				LIN LDO	N LDO OPTIREG™ linear						OPTIRE	G™ SBC						
Cypress	Typical current	Count of CAN	Internal LowVoltage-			Ultra Low Quid	escent Curren	t		Feature Set Watchdog	Lite LDO SBC	MR+ SBC						
PSoC® 4 Family	consomption	controller	Detection *	TLE8457x	TLS805xxx	TLS810xxx	TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx	TLE9461(-3)ES	TLE926x(-3)BQX						
				70mA (5V/3.3V) 1 LIN	50mA (5V/3.3V)	100mA (5V/3.3V)	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)	150mA (5V/3.3V) 1 CAN	≥250mA (5V/3.3V) 1 CAN 0 - 2 LIN						
PSoC 4000(S)		0	no	<b>Ø</b>	<b>©</b>	<b>Ø</b>					<b>©</b>							
PSoC 4100 / PSoC 4100S		0	0	Ü	Ü	Ü	Ü	v	Yes / no	Ø	<b>©</b>	Ø	<b>Ø</b>				<b>Ø</b>	
PSoC 4100S Plus	1.8 V < V <sub>CC</sub> < 5.5 V I <sub>CC</sub> = ~30 mA	1 CAN	no	<b>Ø</b>	<b>⊘</b>	<b>⊘</b>	<b>⊘</b>				<b>Ø</b>	<b>©</b>						
PSoC 4 M Series 4100M		2 CAN	yes								<b>©</b>	Ø						
PSoC 4100S Max						1 CAN-FD	no								<b>Ø</b>	<b>©</b>		







Note: At PSoC® devices without internal LowVoltageDetection the application may require a supply variant with reset output

Further support and calculation tools under www.infineon.com/OPTIREG™



Mapping OPTIREG<sup>™</sup> product portfolio with Texas Instruments Piccolo<sup>™</sup>/
Delfino<sup>™</sup> Microcontrollers

## Mapping OPTIREG™ linear with TI Piccolo™/Delfino™ Microcontroller







Texas Instrument					<b>OPTIREG™</b> linear		
	Texas Instrument C2000™	Power Consumption	Ult	ra Low Quiescent Cu	rrent		d Feature Set nd Watchdog
	Family		TLS810xxx	TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx
			100mA (5V/3.3V)	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)
	TMS320F28004x series	143mA@ 3.3V		<b>©</b>	<b>Ø</b>	<b>©</b>	
	TMS320F2802x series	98mA@ 3.3V	<b>©</b>	<b>©</b>		<b>©</b>	
Piccolo™	TMS320F2802x series	153mA@ 3.3V		Ø	<b>©</b>	<b>©</b>	
generation	TMS320F2805x series	192mA@ 3.3V		Ø	<b>©</b>	<b>©</b>	
	TMS320F2806x series	307mA@ 3.3V					<b>©</b>
	TMS320F2807x series	405mA@ 3.3V					<b>Ø</b>
	TMS320F2833x series*	92mA @ 3.3V 350mA @ 1.8V					
Delfino™	TMS320F2834x series*	80mA @ 3.3V 45mA @ 1.8V 740mA @ 1.2V					
generation*	TMS320F2837xD series*	90mA @ 3.3V 495mA @ 1.8V					
	TMS320F2837xD series*	90mA @ 3.3V 400mA @ 1.2V	<b>⊘</b>	<b>⊘</b>		<b>⊘</b>	







\*Based on **Power Consumption Operational (flash) max.**, see datasheet parameter  $I_{DDIO} + I_{DDA} (+I_{DD3VFL} + I_{DDA33})$  if VREG enabled ( $T_{..} = -40$ °C to 125°C),  $V_{IN}$  (3.3 V)

Further support and calculation tools under <a href="www.infineon.com/OPTIREG™">www.infineon.com/OPTIREG™</a>

## Mapping OPTIREG™ switcher with TI Piccolo™/Delfino™ Microcontroller







			01	PTIREG™ switcher for Pre-	Regulation and Core Vo	Itages
Te	exas Instrument			e-Regulator r DC-DC 500mA		Regulator
	C2000™	Power Consumption	TLF50251	TLF50281	TLS4120D0EP V33	ver Up-to 2.5A TLS4125D0EP V50
	Family		500mA (5V)	500mA (5V)	2000mA (3.3V)	2500mA (5V)
			Enable & Reset	Enable, Reset & Watchdog	Enable + Reset	Enable + Reset
	TMS320F28004x series	143mA@ 3.3V				
	TMS320F2802x series	98mA@ 3.3V				
DiscoloTM generation	TMS320F2802x series	153mA@ 3.3V				
Piccolo <sup>™</sup> generation	TMS320F2805x series	192mA@ 3.3V				
	TMS320F2806x series	307mA@ 3.3V				
	TMS320F2807x series	405mA@ 3.3V			<b>⊘</b>	
	TMS320F2833x series*	92mA @ 3.3V 350mA @ 1.8V				
Dolfing TM goneration*	TMS320F2834x series*	80mA @ 3.3V 45mA @ 1.8V 740mA @ 1.2V				
Delfino <sup>™</sup> generation*	TMS320F2837xD series*	90mA @ 3.3V 495mA @ 1.8V			$\bigcirc$	
	TMS320F2837xD series*	90mA @ 3.3V 400mA @ 1.2V			<b>Ø</b>	







\*Based on **Power Consumption Operational (flash) max.**, see datasheet parameter  $I_{DDIO} + I_{DDA} (+I_{DD3VFL} + I_{DDA33})$  if VREG enabled ( $T_{..} = -40$ °C to 125°C),  $V_{IN}$  (3.3 V)

Further support and calculation tools under <u>www.infineon.com/OPTIREG™</u>

### Mapping OPTIREG™ SBC with TI Piccolo™/Delfino™ Microcontroller







					OPTIREG™ SBC		
Texa	as Instrument	Power	Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC
	C2000™ Family	Consumption	TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX
	i anniy		150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)
Piccolo™	TMS320F28004x series	143mA@ 3.3V	<b>©</b>	<b>©</b>	<b>©</b>		
	TMS320F2802x series	98mA@ 3.3V	<b>©</b>				
	TMS320F2802x series	153mA@ 3.3V		<b>©</b>	<b>©</b>		
generation	TMS320F2805x series	192mA@ 3.3V		<b>©</b>	<b>Ø</b>		
	TMS320F2806x series	307mA@ 3.3V		Ø			
	TMS320F2807x series	405mA@ 3.3V		Ø			<b>②</b>
	TMS320F2833x series*	92mA @ 3.3V 350mA @ 1.8V	$\bigcirc$				
Delfino™	TMS320F2834x series*	80mA @ 3.3V 45mA @ 1.8V 740mA @ 1.2V					
generation*	TMS320F2837xD series*	90mA @ 3.3V 495mA @ 1.8V				<b>Ø</b>	
	TMS320F2837xD series*	90mA @ 3.3V 400mA @ 1.2V	<b>⊘</b>	<b>⊘</b>	<b>⊘</b>	<b>⊘</b>	<b>⊘</b>







\*Based on **Power Consumption Operational (flash) max.**, see datasheet parameter  $I_{DDIO} + I_{DDA} (+I_{DD3VFL} + I_{DDA33})$  if VREG enabled ( $T_{.I} = -40$ °C to 125°C),  $V_{IN}$  (3.3 V)

Further support and calculation tools under <a href="www.infineon.com/OPTIREG™">www.infineon.com/OPTIREG™</a>

### Mapping OPTIREG™ PMIC with TI Piccolo™/Delfino™ Microcontroller







				ОРТ	TREG™ PMIC	
Texa	s Instrument C2000™	Maximum Current	ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant
	Family		TLF35584QV/QK/ QVH	TLF35585QV/QU	TLF30682QV	TLE9243QK
	TMS320F28004x series	143mA@ 3.3V				
Piccolo <sup>TM</sup>	TMS320F2802x series	98mA@ 3.3V				
	TMS320F2802x series	153mA@ 3.3V				
generation	TMS320F2805x series	192mA@ 3.3V				
	TMS320F2806x series	307mA@ 3.3V	<b>Ø</b>	Ø		
	TMS320F2807x series	405mA@ 3.3V	Ø	Ø	<b>⊘</b>	
	TMS320F2833x series*	92mA @ 3.3V 350mA @ 1.8V				
Delfino™	TMS320F2834x series*	80mA @ 3.3V 45mA @ 1.8V 740mA @ 1.2V			<b>Ø</b>	
generation *	TMS320F2837xD series*	90mA @ 3.3V 495mA @ 1.8V				
	TMS320F2837xD series*	90mA @ 3.3V 400mA @ 1.2V	<b>Ø</b>		<b>Ø</b>	<b>⊘</b>

\*Device without internal VREG. Infineon's SBC can only be used to supply 3.3V ( $I_{DDIO} + I_{DD3VFL} + I_{DDA33}$ ) from Vcc1 to as well as the 5V on Vcc2 to supply the CAN transceiver or off-board supply, e.g. for sensor. The core supply  $I_{DD}$  and  $I_{DDA18}$  needs to come from a seperate source.







\*Based on **Power Consumption Operational (flash) max.**, see datasheet parameter  $I_{DDIO} + I_{DDA} (+I_{DD3VFL} + I_{DDA33})$  if VREG enabled ( $T_J = -40$ °C to 125°C),  $V_{IN}$  (3.3 V)

Further support and calculation tools under <a href="www.infineon.com/OPTIREG™">www.infineon.com/OPTIREG™</a>



# Mapping OPTIREG™ product portfolio with NXP S32K Microcontrollers

### Mapping OPTIREG<sup>™</sup> linear with NXP S32K Microcontroller







					OPTIREG™	linear		
9	NXP 32K1xx	Maximum	Ultra	a Low Quiescent Curre	nt	Advanced Feature Set Reset and Watchdog		Post LDO / Core Voltage
	Family	Power Consumption	TLS810xxx	TLS820xxx	TLS835xxx	TLS820Fx	TLS850Fx	TLS20x Family
	Í		100mA (5V/3.3V)	200mA (5V/3.3V)	350mA (5V/3.3V)	200mA (5V/3.3V)	500mA (5V/3.3V)	Use in combination with pre dc-dc
	S32K116 <sup>1</sup>	24.1mA @ 5V	Ø					
	S32K118 <sup>1</sup>	25.9mA @ 5V	Ø					
S32K	S32K142 <sup>2</sup>	57.4mA @ 5V	Ø					
332N	S32K144²	61.3mA @ 5V	Ø					
	S32K146 <sup>2</sup>	82.8mA @ 5V	Ø	<b>Ø</b>		<b>Ø</b>		
	S32K148 <sup>2</sup>	97.4mA @ 5V 119mA @ 3.3V						TLS203x (300mA)

 $<sup>^{2}</sup>$  Max. 112 MHz @  $T_{A} = 105$ °C







Note: Based on **Power Supply Current Flash max.**, see datasheet parameter I<sub>CC5</sub>

Further support and calculation tools under <a href="www.infineon.com/OPTIREG™">www.infineon.com/OPTIREG™</a>

<sup>&</sup>lt;sup>1</sup> Max. 48 MHz @ T<sub>A</sub> = 125°C

### Mapping OPTIREG<sup>™</sup> switcher with NXP S32K Microcontroller







			OPTIREG™ switcher for Pre	-Regulation and Core Voltages		
	NXP	Maximum		-Regulator DC-DC 500mA	Post LDO / Core Voltage	
	S32K1xx Family	Power Consumption	TLF50251	TLF50281	TLS20x Family	
	1 anny		500mA (5V)	500mA (5V)	Use in combination with pre dc-dc	
			Enable & Reset	Enable, Reset & Watchdog		
	S32K116 <sup>1</sup>	24.1mA @ 5V				
	S32K118 <sup>1</sup>	25.9mA @ 5V				
00014	S32K142 <sup>2</sup>	57.4mA @ 5V				
S32K	S32K144 <sup>2</sup>	61.3mA @ 5V				
	S32K146 <sup>2</sup>	82.8mA @ 5V				
	S32K148 <sup>2</sup>	97.4mA @ 5V 119mA @ 3.3V			TLS203x (300mA)	

 $<sup>^{2}</sup>$  Max. 112 MHz @  $T_{A} = 105$ °C







Note: Based on **Power Supply Current Flash max.**, see datasheet parameter I<sub>CC5</sub>

Further support and calculation tools under www.infineon.com/OPTIREG™

<sup>&</sup>lt;sup>1</sup> Max. 48 MHz @ T<sub>A</sub> = 125°C

### Mapping OPTIREG<sup>™</sup> SBC with NXP S32K Microcontroller







			OPTIREG™ SBC						
6224	NXP	Maximum Power Consumption	Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC		
332r	(1xx Family		TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX		
			150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)		
	S32K116 <sup>1</sup>	24.1mA @ 5V	Ø						
	S32K118 <sup>1</sup>	25.9mA @ 5V	<b>©</b>						
6221/	S32K142 <sup>2</sup>	57.4mA @ 5V	<b>©</b>						
S32K	S32K144 <sup>2</sup>	61.3mA @ 5V	Ø						
	S32K146 <sup>2</sup>	82.8mA @ 5V	Ø						
	S32K148 <sup>2</sup>	97.4mA @ 5V 119mA @ 3.3V	<b>⊘</b>						

 $<sup>^{2}</sup>$  Max. 112 MHz @  $T_{A} = 105$ °C







Note: Based on **Power Supply Current Flash max.**, see datasheet parameter I<sub>CC5</sub>

Further support and calculation tools under www.infineon.com/OPTIREG™

<sup>&</sup>lt;sup>1</sup> Max. 48 MHz @ T<sub>A</sub> = 125°C



# Mapping OPTIREG™ PMICs & SBCs with Renesas RH850 Microcontrollers

### Mapping OPTIREG<sup>™</sup> PMIC with Renesas RH850 Microcontroller







			OPTIREG™ PMIC				
	RH850 Family	Maximum Power dissipation	ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant	
	Kiloso i allilly		TLF35584QV/QK/ QVH	TLF35585QV/QU	TLF30682QV	TLE9243QK	
C Series	RH850/C1M-Ax	730mA @ 1.25V 112mA @ 3.3V/5V			<b>©</b>		
	RH850/E1L	260mA @ 1.1V (NN) 53mA @ 3.3V/5V					
E Series	RH850/E2H	1300mA @ 1.1V 152mA @ 3.3V/5V					
L Oeries	RH850/E2M	1500mA @ 1.1V 154mA @ 3.3V/5V					
	RH850/E2UH	1700mA @ 1.1V 156mA @ 3.3V/5V					
	RH850/F1K	96mA @ 3.3V/5V					
F Series	RH850/F1H-D8	350mA tot @ 3.3V/5V (REG1VCC 290mA @ 3.3V)	Ø	Ø			
r Series	RH850/F1KM-S1	82mA @ 3.3V/5V					
	RH850/F1KM-S4	205mA @ 3.3V/5V					







## Mapping OPTIREG™ SBC with Renesas RH850 Microcontroller







Renesas RH850			OPTIREG™ SBC					
		Power Consumption	Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC	
	amily	. Choi Concampaion	TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX	
			150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)	
C Series	RH850/C1M-Ax	730mA @ 1.25V 112mA @ 3.3V/5V						
	RH850/E1L	260mA @ 1.1V (NN) 53mA @ 3.3V/5V						
E Series	RH850/E2H	1300mA @ 1.1V 152mA @ 3.3V/5V						
	RH850/E2M	1500mA @ 1.1V 154mA @ 3.3V/5V						
	RH850/E2UH	1700mA @ 1.1V 156mA @ 3.3V/5V						
	RH850/F1K	96mA @ 3.3V/5V	<b>©</b>		<b>⊘</b>			
F Series	RH850/F1KH-D8	350mA tot @ 3.3V/5V (REG1VCC 290mA @ 3.3V)		Ø		<b>⊘</b>	<b>⊘</b>	
	RH850/F1KM-S1	82mA @ 3.3V/5V	<b>©</b>					
	RH850/F1KM-S4	205mA @ 3.3V/5V		Ø	<b>©</b>		<b>⊘</b>	







Further support and calculation tools under <a href="www.infineon.com/OPTIREG™">www.infineon.com/OPTIREG™</a>



## Mapping OPTIREG<sup>™</sup> PMICs & SBCs with ST SPC5x Microcontrollers

### Mapping OPTIREG<sup>™</sup> PMIC with ST SPC5x Microcontroller







	Maximum Power dissipation	OPTIREG™ PMIC					
ST SPC5x Family		ISO 26262 compliant	ISO 26262 compliant		ISO 26262 compliant		
		TLF35584QV/QK/ QVH	TLF35585QV/QU	TLF30682QV	TLE9243QK		
SPC584C70E3	711mA @ 3.3V/5V						
SPC584B60E1	390mA @ 3.3V/5V	<b>©</b>	<b>©</b>				
SPC58NH92C3	1800mA @ 3.3V/5V			<b>Ø</b>			
SPC560B64L7	386mA @ 3.3V/5V	<b>©</b>	<b>©</b>				
SPC56EL70L5	460mA @ 3.3V	Ø	<b>©</b>				
SPC564A80B4	546mA @ 5V	<b>©</b>	Ø		Ø		







## Mapping OPTIREG<sup>™</sup> SBC with ST SPC5x Microcontroller







	Power Consumption	OPTIREG™ SBC						
ST SDCEv Family		Lite LDO SBC	Lite DCDC SBC	MR+ SBC	DCDC SBC	MCP+ SBC		
ST SPC5x Family		TLE946x(-3)ES	TLE947x(-3)ES	TLE926x(-3)BQX	TLE927xQX	TLE9278(-3)BQX		
		150mA (5V/3.3V)	500mA (5V/3.3V)	≥250mA (5V/3.3V)	750mA (5V/3.3V)	750mA (5V/3.3V)		
SPC584C70E3	711mA @ 3.3V/5V				Ø			
SPC584B60E1	390mA @ 3.3V/5V		<b>©</b>		Ø	<b>©</b>		
SPC58NH92C3	1800mA @ 3.3V/5V							
SPC560B64L7	386mA @ 3.3V/5V		Ø		Ø	<b>©</b>		
SPC56EL70L5	460mA @ 3.3V		<b>©</b>		<b>©</b>	<b>©</b>		
SPC564A80B4	546mA @ 5V				Ø	<b>©</b>		







