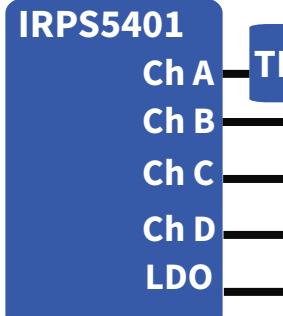


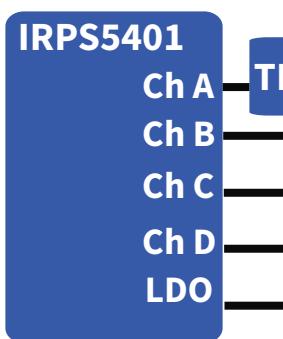
Zu02/03/04/05/06/07/09
CG - EG - EV Series
No SERDES
Always On



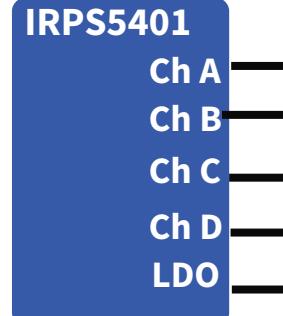
Zu02 (<7A)	Zu06 (<21A)
Zu03 (<8A)	Zu07 (<22A)
Zu04 (<14A)	Zu09 (<24A)
Zu05 (<16A)	TDA21240 (<25A)
TDA21242 (<16A)	
Configuration 4	Configuration 5
Ch A	TDA21240/2
Ch B	1 Core Voltage 0.85V / 0.9V - 16A
Ch C	2 DDR (VCCO_PSDDR4) 1.2V - 1.5A
Ch D	3 PS I/O supply 1.8V - 1A
LDO	4 Auxiliary supply 1.8V - 4A
	5 PS PLL supply 1.2V - 0.5A

IR3883 — System Voltage 3.3V

Zu02/03/04/05/06/07/09
CG - EG - EV Series
With SERDES
Always On



Zu02 (<7A)	Zu06 (<21A)
Zu03 (<8A)	Zu07 (<22A)
Zu04 (<14A)	Zu09 (<24A)
Zu05 (<16A)	TDA21240 (<25A)
TDA21242 (<16A)	
Configuration 4	Configuration 5
Ch A	TDA21240/2
Ch B	1 Core Voltage 0.85V / 0.9V - 16A
Ch C	2 DDR (VCCO_PSDDR4) 1.2V - 1.5A
Ch D	3 PS I/O supply 1.8V - 1A
LDO	4 Auxiliary supply 1.8V - 4A
	5 PS PLL supply 1.2V - 0.5A



System Voltage 3.3V	3.3V - 1.5A	3.3V - 1.5A
Ch B	6 SERDES 1.8V 1.8V - 1A	1.8V - 1A
Ch C	7 SERDES 0.9V 0.9V - 2A	0.9V - 2A
Ch D	5 SERDES 1.2V 1.2V - 2A	1.2V - 2A
LDO	8 SERDES 0.85V 0.85V - 0.5A	0.85V - 0.5A

Note: For Zu02 and Zu03 Configuration 6 not needed for SERDES
SERDES option for Zu04 and above

Zu02 (<4A)
Zu04/05/06/07/09
CG - EG Series
No SERDES
Always On - Power Efficiency

Zu02 (<4A)	Zu06 (<17A)
Zu03 (<5A)	Zu07 (<15A)
Zu04 (<7A)	Zu09 (<20A)
Zu05 (<9A)	TDA21240 (<25A)
TDA21242 (<16A)	
Configuration 11	Configuration 12
Ch A	TDA21240/2
Ch B	1a Core Voltage, 0.72V 0.72V / 0.85V / 0.9V - 9A
Ch C	2 DDR (VCCO_PSDDR4) 1.2V - 1.5A
Ch D	1b Core Voltage, 0.85V 0.85V - 4A
LDO	4 Auxiliary supply 1.8V - 2A
	5 PS PLL supply 1.2V - 0.5A

IR3883	System Voltage 3.3V
IR3883	3 PS I/O: VCCO_PSIO (0:3) 1.8V / 500mA - 1.5A

Xilinx
Zynq UltraScale+ MPSoC
Power Macros for
Zu02 to Zu09
CG / EG / EV SERIES

Part Number: 12 Configurations
IRPS5401MXI03TRP

Zu02/03/04/05/06/07/09
CG - EG Series
With SERDES
Always On - Power Efficiency

Zu02 (<4A)	Zu06 (<17A)
Zu03 (<5A)	Zu07 (<15A)
Zu04 (<7A)	Zu09 (<20A)
Zu05 (<9A)	TDA21240 (<25A)
TDA21242 (<16A)	
Configuration 11	Configuration 12
Ch A	TDA21240/2
Ch B	1a Core Voltage, 0.72V 0.72V / 0.85V / 0.9V - 9A
Ch C	2 DDR (VCCO_PSDDR4) 1.2V - 1.5A
Ch D	1b Core Voltage, 0.85V 0.85V - 4A
LDO	4 Auxiliary supply 1.8V - 2A
	5 PS PLL supply 1.2V - 0.5A

IR3883	3 PS I/O: VCCO_PSIO (0:3) 1.8V / 500mA - 1.5A
IRPS5401	Configuration 6

Ch A	System Voltage 3.3V	3.3V - 1.5A	3.3V - 1.5A
Ch B	6 SERDES 1.8V	1.8V - 1A	1.8V - 1A
Ch C	7 SERDES 0.9V	0.9V - 2A	0.9V - 2A
Ch D	5 SERDES / PLL 1.2V	1.2V - 2A	1.2V - 2A
LDO	8 SERDES 0.85V	0.85V - 0.5A	0.85V - 0.5A

Zu04EV / Zu05EV / Zu07EV
EV Series
No SERDES
Always On - Power Efficiency

Zu04 (<7A)
Zu05 (<9A)
TDA21242 (<16A)

Zu07 (<15A)
TDA21240 (<25A)

Configuration 7

Configuration 8

IRPS5401	TDA21242	1a Core Voltage, 0.72V	0.72V / 0.85V / 0.9V - 9A	0.72V / 0.85V / 0.9V - 15A
Ch A				
Ch B		System Voltage 3.3V	3.3V - 2A	3.3V - 2A
Ch C		Core Voltage, 0.85V	0.85V - 7A	0.85V - 7A
Ch D				
LDO		5 PS PLL supply	1.2V - 0.5A	1.2V - 0.5A
IRPS5401		Configuration 9	Configuration 9	
Ch A		3 PS I/O supply	1.8V - 1A	1.8V - 1A
Ch B		2 DDR (VCCO_PSDDR4)	1.2V - 1.5A	1.2V - 1.5A
Ch C		4 Auxiliary supply	1.8V - 4A	1.8V - 4A
Ch D		System Voltage 2.5V	2.5V - 1A	2.5V - 1A
LDO		9 User I/O: 1.5V, 500mA	1.5V - 0.5A	1.5V - 0.5A

Zu04EV / Zu05EV / Zu07EV
EV Series
With SERDES

Always On - Power Efficiency

Zu04 (<7A)
Zu05 (<9A)
TDA21242 (<16A)

Configuration 7

Configuration 8

IRPS5401	TDA21242	1a Core Voltage, 0.72V	0.72V / 0.85V / 0.9V - 9A	0.72V / 0.85V / 0.9V - 15A
Ch A				
Ch B		System Voltage 3.3V	3.3V - 2A	3.3V - 2A
Ch C		Core Voltage, 0.85V	0.85V - 7A	0.85V - 7A
Ch D				
LDO		5 PS PLL supply	1.2V - 0.5A	1.2V - 0.5A
IR3883		3 PS I/O supply	1.8V - <1.5A	1.8V - <1.5A

IRPS5401		Configuration 10	Configuration 10	
Ch A	7 SERDES 0.9V	0.9V - 2A	0.9V - 2A	
Ch B	2 DDR (VCCO_PSDDR4)	1.2V - 1.5A	1.2V - 1.5A	
Ch C	4 Auxiliary supply	1.8V - 2A	1.8V - 2A	
Ch D	5 SERDES / PLL 1.2V	1.2V - 2A	1.2V - 2A	
LDO	8 SERDES 0.85V	0.85V - 0.5A	0.85V - 0.5A	
LDO	6 SERDES 1.8V	1.8V - 0.1A	1.8V - 0.1A	

Infineon DC/DC PMIC	MTP Resistor Address	Application – Voltage Outputs	PowerStage	EXT_A INDUCTOR	Zynq US+ config #
IRPS5401MXI03TRP	8870	1.8/1.8/.85/.85/1.2	N/A		1
	10000	1.2/.85/3.3/.85/1.8	N/A		2
	11000	1.8/5/3.3/.85/1.8	N/A		3
	2320	.85/1.2/1.8/1.8/1.2	TDA21242	VLB10050HT-R20M	4
	2870	.85/1.2/1.8/1.8/1.2	TDA21240	VLB10050HT-R12M	5
	3480	3.3/1.8/.9/1.2/.85	N/A		6
	4120	.72/3.3/.85/1.2	TDA21242	VLB10050HT-R30M	7
	4750	.72/3.3/.85/1.2	TDA21240	VLB10050HT-R20M	8
	5490	1.8/1.2/1.8/2.5/1.5	N/A		9
	6190	.9/1.2/1.8/1.2/.85	N/A		10
	6980	.72/1.2/.85/1.8/1.2	TDA21242	VLB10050HT-R30M	11
	7870	.72/1.2/.85/1.8/1.2	TDA21240	VLB10050HT-R20M	12

For Zu11 to Zu19:

For Zu15 use Zu09 Configuration 5 & 6

For Zu11 use Zu09 Configuration 5 & 6 (for SERDES <2A)
if SERDES < 6A, use IR38060 for MGTAVCC/VTT

For Zu17/Zu19 use Zu09 Configuration 5 & 6 (for SERDES <2A)
if SERDES < 6A, use IR38060 for MGTAVCC/VTT
if SERDES <10A, use IR38062 for MGTAVTT

Xilinx
Zynq UltraScale+ MPSoC
Power Macros for
Zu02 to Zu09
CG / EG / EV SERIES

Part Number: 12 Configurations
IRPS5401MXI03TRP